Fig.1 (Prior Art)

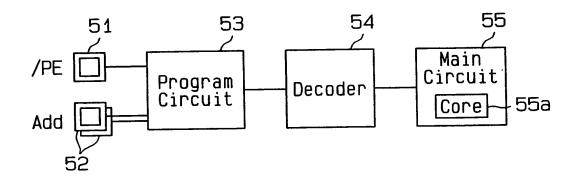
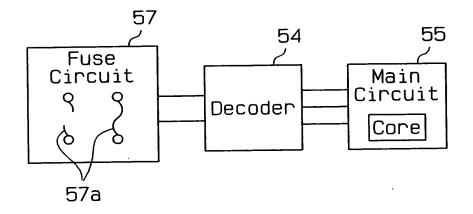
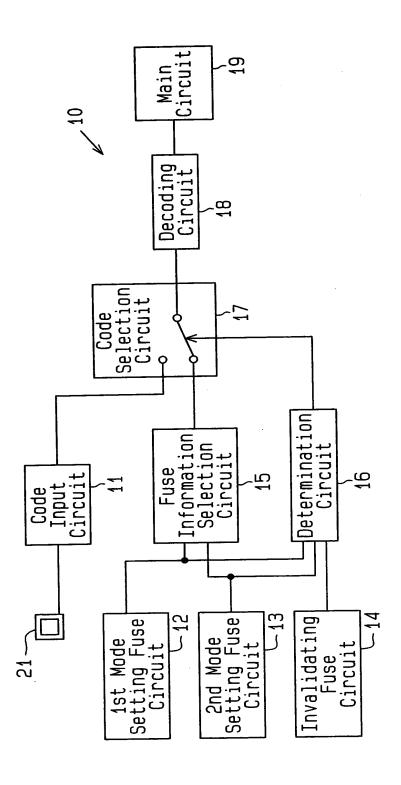


Fig.2(Prior Art)



SEMICONDUCTOR DEVICE
Inventor's Name: KURITA
Application No.: NEW
Docket No.: 108075-00111

Fig.3



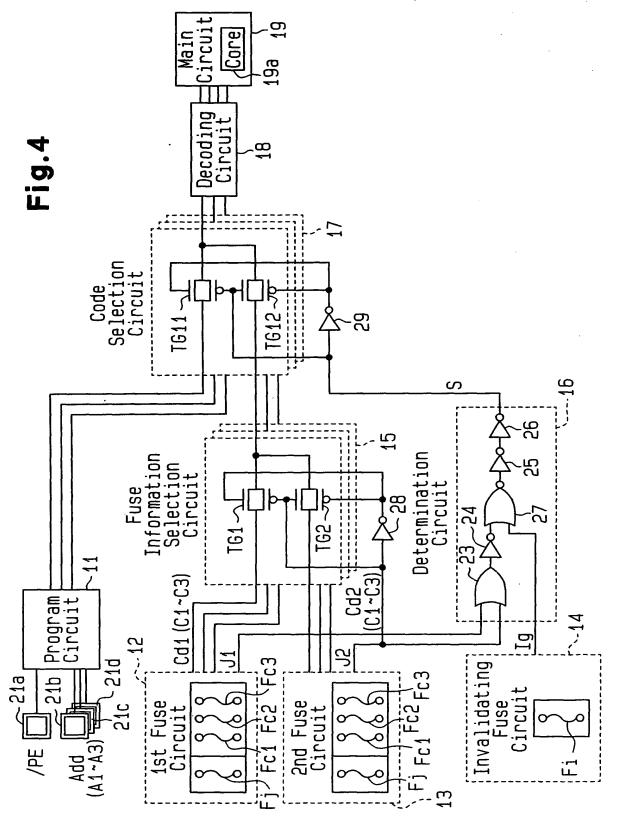


Fig.5

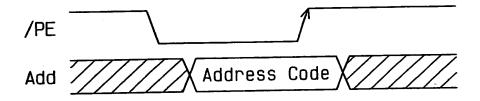


Fig.6

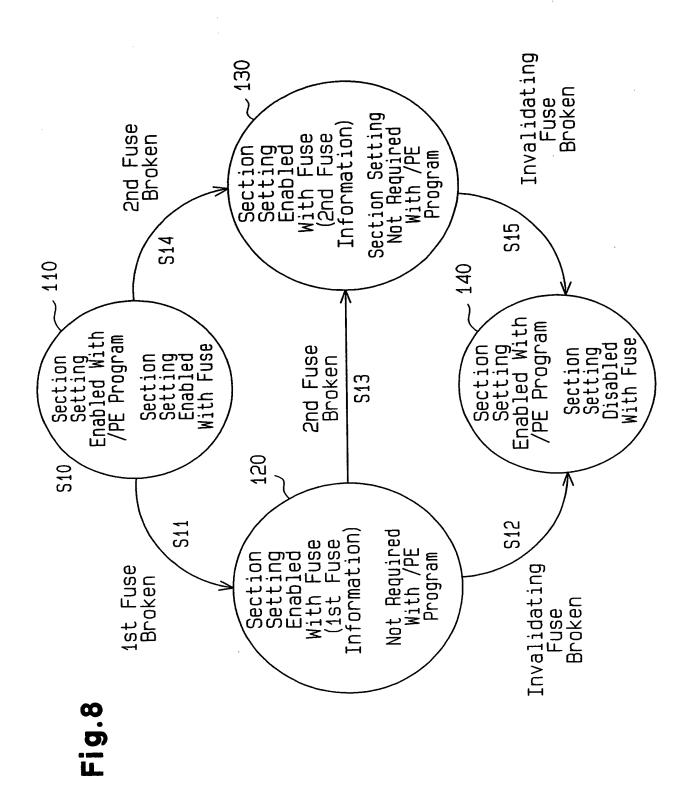
A1	Α2	АЗ	Refreshing Section		
Н	L	L	32Mb Upper Addresses		
L	Н	L	16Mb Upper Addresses		
Н	L	Н	32Mb Lower Addresses		
L	Н	Н	16Mb Lower Addresses		

Fig.7

	Fuse	Refreshing Section		
Fc1	Fc2	Fc3	Hell Calling accertain	
Broken	Not Broken	Not Broken	32Mb Upper Addresses	
Not Broken	Broken	Not Broken	16Mb Upper Addresses	
Broken	Not Broken	Broken	32Mb Lower Addresses	
Not Broken	Broken	Broken	16Mb Lower Addresses	

Title: SEMICONDUCTOR DEVICE AND

METHOD FOR CONTROLLING SEMICONDUCTOR DEVICE Inventor's Name: KURITA Application No.: NEW Docket No.: 108075-00111



Title: SEMICONDUCTOR DEVICE AND METHOD FOR CONTROLLING SEMICONDUCTOR DEVICE Inventor's Name: KURITA

Application No.: NEW Docket No.: 108075-00111

Fig.9

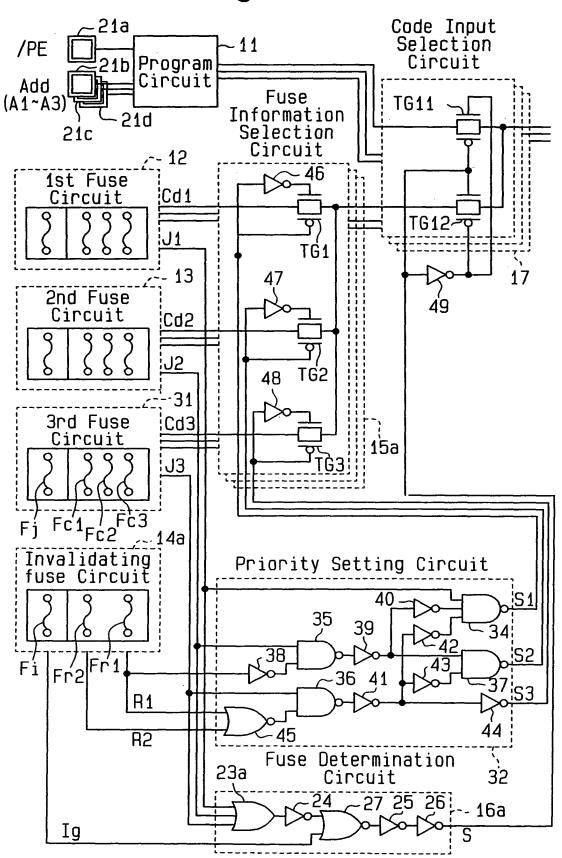


Fig.10

